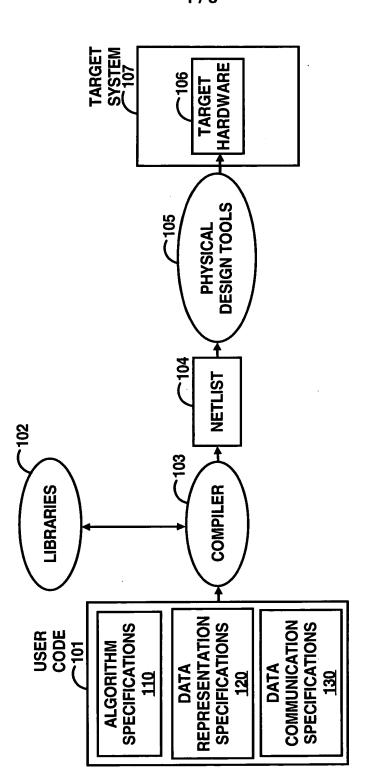
100

AN ALGORITHM-TO-HARDWARE SYSTEM AND METHOD FOR CREATING A DIGITAL CIRCUIT

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FIG

AN ALGORITHM-TO-HARDWARE SYSTEM AND METHOD FOR CREATING A DIGITAL CIRCUIT Richard J. CARTER HP Docket No.: 10008130-1

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200

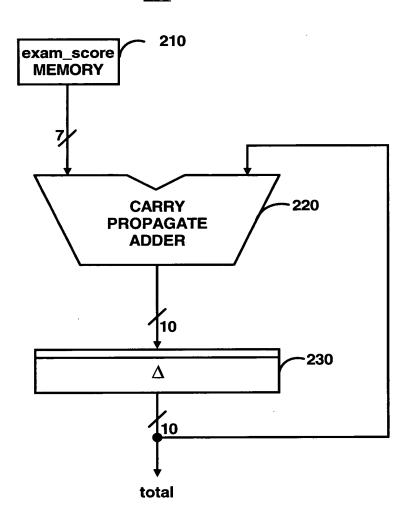


FIG. 2

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<u>300</u>

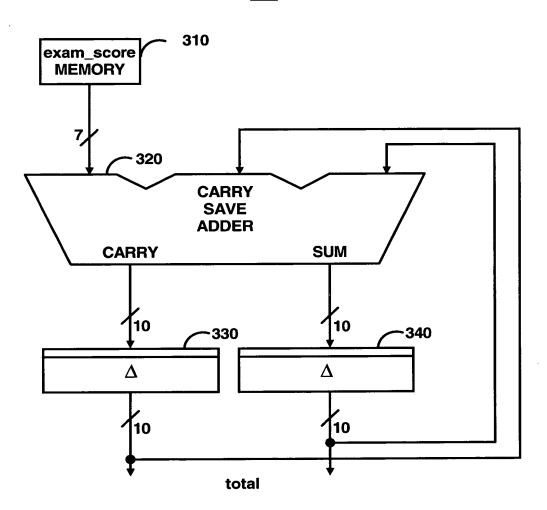


FIG. 3

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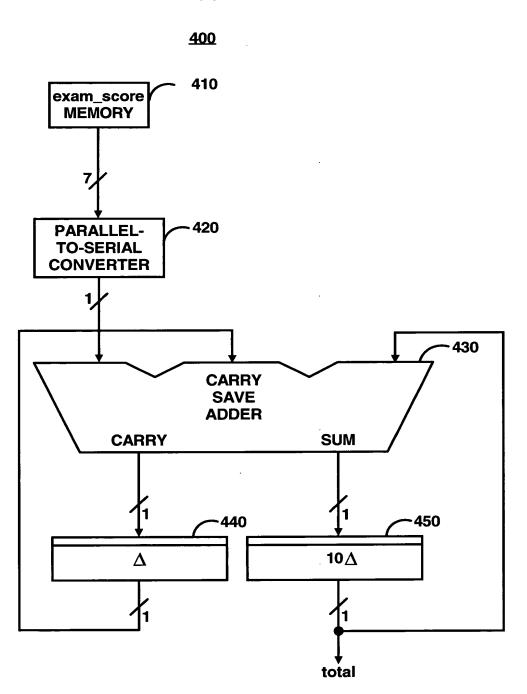


FIG. 4

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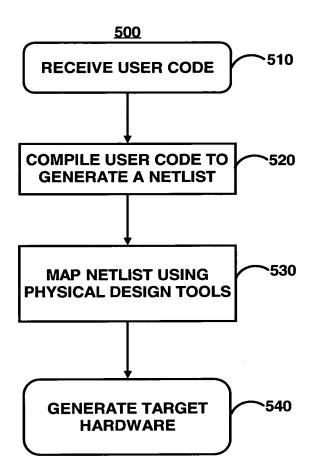


FIG. 5

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<u>600</u>

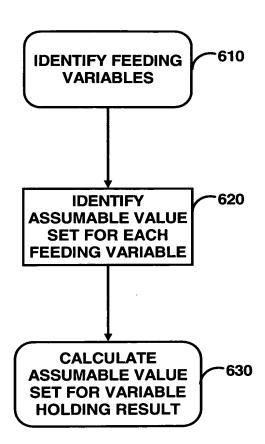


FIG. 6

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700

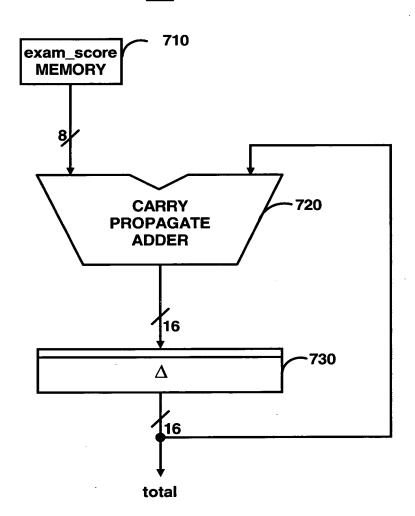
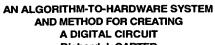


FIG. 7 Prior Art



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<u>800</u>

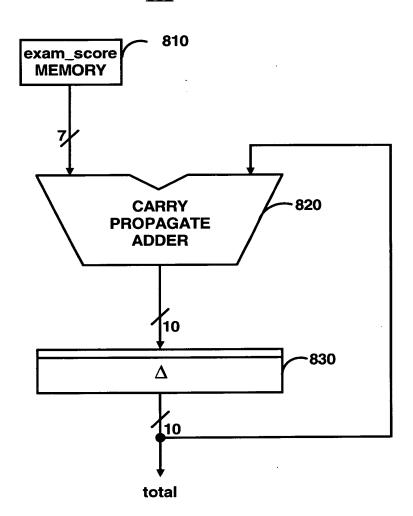


FIG. 8 Prior Art